USING ASSEMBLER FOR MICROCONTROLLER STUDY ON ARDUINO-BASED PLATFORM

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Abstract. Improving the information systems is a high-priority direction for the development of world science, engineering and technology. Graduates of engineering faculties could be involved in this trend in specific sectors of industry in the case they will master real-time systems programming in the process of education at higher school. These systems are built on the basis of miniature computers – microcontrollers implementing the basic functions of classic information systems. The purpose of this article is to show the didactic possibilities of using Arduino hardware platform together with the methods of programming in Assembly language. To achieve this goal a classic example of square wave generator programming on the Arduino hardware platform in professional development environment AVR Studio in Assembly language is considered. Experience of Arduino programming in high-level languages C, C++ is presented in engineering education. In this work we seek to actualize didactic capabilities of Assembler, which were not used in the educative process: accessibility of all elements of the microcontroller architecture that allows a student developing the program to implement the most efficient algorithms; ability to generate machine instructions and to control this process by a student; visibility of the microcontroller work, which increases the level of understanding of processes occurring in the system, and, consequently, the effectiveness of training. Assembler is the foundation in the field of microprocessor technique cognition through the study of its programming methods. Arduino hardware platform is made on the microcontrollers of AVR family. These microcontrollers are in the ratio of price – performance – power consumption occupy leading positions and are recognized as industry standard. The study of microcontrollers, which is organized in a professional development environment by programming in Assembly language, is a variant of a practice-oriented cognitive technology in engineering education.

Keywords: algorithm, program, real-time information systems, experiment, generator, oscilloscope, AVR Studio

Introduction

Modern electronic devices are built on the basis of microcontrollers. A microcontroller is designed to implement the basic functions of real-time information systems: collection, processing, storage and transmission of information, as well as the formation of the control signals. The field-of-use microcontrollers are wider than microprocessors. They are used in control systems, ranging from household appliances and ending aircraft. A microcontroller contains on a single semiconductor chip almost all digital electronics devices, from the logic elements to processor. Each family of microcontrollers has its own Assembly language.

When programming in Assembly Language for a developer all the capabilities of the microcontroller architecture are accessed, and he can use them to implement the most efficient algorithms. High-level languages do not provide such an opportunity. When programming a microcontroller in high level language, such as C, it is not required to know its architecture. Assembler Language is the foundation in the cognition field of microprocessor technique through the study of software designing processes. Figuratively speaking, Assembler is the language of unlimited possibilities [1].

Arduino-based platform allows realizing a process of learning using the Assembly. Arduino is the most affordable budget solution, implemented at the popular family of AVR microcontrollers. While programming Arduino there are the most actual for information real-time systems languages C/C++ and Assembler used [2]. Arduino contains an extensive assortment of modular hardware and software with an open source license, continues to develop and is supported by sufficient amount of literature [3-5]. Arduino is supported by: the widespread in universities Matlab tool [6]; AVRStudio7 software environment designed for the professional development of software application for 8-bit and 32-bit AVR microcontrollers [7]; the new operating system Windows 10 [8]. Briefly you can get acquainted with Arduino on the site https://www.arduino.cc.

The authors do not know works that discuss projects aimed at implementation of typical tasks of real-time information systems using Assembly on the Arduino-based platform. The purpose of the
The article is to consider a case that is most suitable for the initial stage of microcontroller study and show that hardware platform Arduino can be used to study microcontrollers on Assembler.

**Materials and methods**

We are implementing the square wave generator (hereinafter simply generator) on the Arduino-based hardware. A generator can be realized as hardware or software way. A hardware generator requires the using of a timer/counter (an inbuilt peripheral) of a microcontroller and is relatively complicated for an initial study of microcontrollers. We are realizing the programmable generator [9]. For programming in Assembly language we use the integrated development environment AVR Studio, which is distributed free and always available on Atmel’s website. The actual version is AVR Studio 4.18.684.

A timing diagram of the generator signal is shown in Figure 1. The signal period is determined by the expression: $T_G = t_i + t_p$, where $t_i$ and $t_p$, respectively, pulse (impulse) duration and pause duration. To simplify the task, we take $t_i = t_p$.

![Fig. 1. Generator signal, Timing Diagram](image)

The algorithm for the GENERATOR program is as follows:

1. **Step 1**: Start
2. **Step 2**: Output a high voltage level to the PD0 pin of the microcontroller, i.e. logical ‘1’.
3. **Step 3**: Form a time delay “DELAY” with duration $t_i$.
4. **Step 4**: Output a low voltage level to the microcontroller PD0 pin, i.e. logical ‘0’.
5. **Step 5**: Create a time delay “DELAY” with duration $t_p$ and go to Step 2.
6. **Step 6**: Stop

A properly designed program should consist of separate functionally complete debugged modules – subroutines. Consider the subroutine DELAY, which is part of the program GENERATOR. The algorithm for the DELAY subroutine is shown in Figure 2.

![Fig. 2. Algorithm of the DELAY subroutine](image)

The subroutine implementing the DELAY algorithm is presented in Figure 3.

```
DELAY: ; The time delay subroutine
LDI R16,5 ; Step 1. Load into the register R16 the constant 5 (X = 5)
ml: DEC R16 ; Step 2. Decrement the contents of register R16
BRNE ml ; Step 3. If not zero go to ml
RET ; Exit the DELAY subroutine
```

![Fig. 3. DELAY subroutine](image)
To debug the DELAY subroutine, a code fragment that simulates the main program is used. This code fragment is presented below in Figure 4.

<table>
<thead>
<tr>
<th>m2:</th>
<th>RCALL DELAY ; Call subroutine DELAY</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>RJMP m2 ; Go to m2</td>
</tr>
</tbody>
</table>

Fig. 4. Code fragment

The DELAY subroutine is called from the main program by the RCALL DELAY command (the first line with the m2 mark). After execution of the DELAY subroutine, the control is transferred to the main program with the RET command (see the DELAY subroutine). Then the RJMP m2 command is executed – an unconditional jump to the m2 mark (the second line of the DELAY subroutine debug fragment).

To debug the DELAY subroutine, you can select the value of the constant \( X \) from the range: 1 ... 255, where 255 is the decimal equivalent of the maximum binary code that can be loaded into the 8-bit microcontroller register. In the considered case \( X = 5 \). We check the operability of the DELAY subroutine in the step-by-step debugging mode in AVR Studio environment. If the subroutine works, then we go to calculate the time delay of the specified duration, i.e. to the calculation of the constant \( X \) (see the algorithm presented in Figure 2).

For example, it is required to develop a generator that forms a rectangular signal with a frequency \( f_G = 20 \text{ kHz} \). To solve this task, it is necessary to calculate the delay time \( T_D \), which is determined from the condition \( T_D = t_i + t_p \). The generator period is defined by the expression: \( T_G = 1 / f_G = 1/20000 = 50 \mu \text{s} \). As \( T_G = t_i + t_p = 2T_D \), then \( T_D = T_G / 2 = 25 \mu \text{s} \).

The time delay duration is mainly determined by the cycle amount that the microcontroller must perform when executing the DELAY subroutine. The cycle consists of two commands with the mnemonics DEC and BRNE (see the DELAY subroutine). The remaining commands with mnemonics LDI, RET and RCALL are executed by the microcontroller only once.

A microcontroller operates under the action of a clock generator. The number of clock cycles required for the microcontroller to execute various commands can be determined according to the Complete Instruction Set Summary [10]. The number of cycles needed for the microcontroller to execute RCALL, LDI and RET mnemonics is given by:

\[
N_{RLR} = N_{RCALL} + N_{LDI} + N_{RET},
\]

where \( N_{RCALL} \), \( N_{LDI} \) and \( N_{RET} \) – number of clock cycles required for the microcontroller to execute RCALL, LDI and RET mnemonics, respectively.

Then according to expression (1): \( N_{RLR} = 3 + 1 + 4 = 8 \) cycles.

The number of cycles needed for the microcontroller to implement the time delay of a given duration is determined by the expression:

\[
X = (T_D - T_{RLR})/T_{DB}
\]

where \( T_{RLR} \) – time required for the microcontroller to perform RCALL, LDI and RET mnemonics; \( \mu \text{s} \); \( T_{DB} \) – time required for the microcontroller to perform DEC and BRNE mnemonics, i.e. a single cycle, \( \mu \text{s} \).

\( T_{RLR} \) time is determined from the expression:

\[
T_{RLR} = T_c \cdot N_{RLR},
\]

where \( T_c \) – clock period of the microcontroller, determined from the expression \( T_c = 1/fc \); \( fc \) – clock frequency of the microcontroller, \( f_c = 16 \text{ MHz} \) is specified in the description of the Arduino Uno hardware (https://www.arduino.cc).

Then \( T_c = 1/16 \cdot 10^6 = 0.0625 \mu \text{s} \).

In accordance with the expression (3), \( T_{RLR} = 0.0625 \cdot 8 = 0.5 \mu \text{s} \).

\( T_{DB} \) time is defined by the formula:

\[
T_{DB} = T_c \cdot N_{DB}.
\]
where $N_{DB}$ is the number of cycles required for the microcontroller to perform a single cycle, i.e. DEC and BRNE mnemonics.

In accordance with the Complete Instruction Set Summary [10], we define $N_{DB} = 1 + 2 = 3$ clocks. Then, according to formula (4), we calculate $T_{DB} = 0.0625 \cdot 3 = 0.1875 \, \mu s$.

From expression (2) we define: $X = (25 - 0.5) / 0.1875 = 130.66 \approx 131$ cycles.

In the subroutine DELAY we change the initial value of the constant $X$ to the resulting, that is, $X = 131$ and check the time value forming by the DELAY subroutine in the AVR Studio environment in debug mode (Figure 5).

![Fig. 5. AVR Studio environment window in debug mode of DELAY subroutine](image)

If the DELAY subroutine generates a time delay of the given duration, as seen in the Processor window (Stop Watch 25.00 us), then we go to the development of the program that implements the algorithm of the generator presented above. Debugged in AVR Studio the GENERATOR program is shown in Figure 6.

```assembly
.include"m328Pdef.inc"

;Include the name mapping files of I/O registers
;for ATmega328 microcontroller addresses

;Initialize the stack
LDI R16, high (RAMEND) ;Write to the stack pointer register
OUT SPH, R16  ;high byte of the RAMEND constant
LDI R16, low (RAMEND) ;Write to the stack pointer register
OUT SPL, R16  ;low byte of the RAMEND constant

;Initialize the HD Port
LDI R16, 0b00000001 ;Configure the PDO line to the output
OUT DDRD,R16

GENERATOR: ;The program “Generator of rectangular impulses (GRI)”
m1: SBI PORTD, 0 ;Output logical '1' to PD0 pin
RCALL DELAY ;Call DELAY subroutine
CBI PORTD, 0 ;Output logical '0' to PD0 pin
RCALL DELAY

RJMP m1 ;Repeat the period output

DELAY: ;The time delay subroutine of 25 \mu s, with \(f_c = 16\, MHz\)
LDI R16, 131 ;Load number 131 in the R16 register
m2: DEC R16 ;Decrement of register R16 content
BRNE m2 ;If register R16 content is not zero, go to m2
RET ;Return from DELAY
```

![Fig. 6. GENERATOR program](image)

Figure 7 shows a fragment of the GENERATOR program debugging mode in AVR Studio environment. After debugging, we record the GENERATOR program into the microcontroller memory using the Atmel AVRISP mkII programmer [11]. It connects to the Arduino Uno through a special connector. GENERATOR.hex file containing the program in the machine code language is located in the project folder.

Figure 8 shows the oscillogram of the signal formed by the Arduino-based generator. From the oscillogram it is seen that the time parameters of the signal almost correspond to the set values. At a given $f_0 = 20$ kHz, the signal frequency is 19.9 kHz. For a given $t_i = 25$ us, the pulse width is 25.1 us.
Fig. 7. GENERATOR program debugging window in AVR Studio

Fig. 8. Generator signal at the ATmega328 microcontroller PD0 output, Oscillogram

Minor discrepancies between the experimental parameters of the signal and the given ones can be explained by the fact that the calculations did not take into account the time necessary for the microcontroller to execute the commands:

- SBI PORTD, 0 ;Output logical ‘1’ to PD0 pin
- CBI PORTD, 0 ;Output logical ‘0’ to PD0 pin
- RJMP m1 ;Repeat the period output

In experimental studies were used the devices: Arduino Uno, AVRISPmkII programmer, DSO-2090 USB Oscilloscope and Computer.

Results and discussion

The hypothesis of the expediency of using Assembler for forming the students’ universal learning action for programming microcontrollers was confirmed by the survey and responses on control questions among the students of the experimental groups. 49 students of the third year of the Electric Power Faculty took part in the survey and control testing: the 1st experimental group of students in the major of “Electric power engineering and electrical engineering” consisted of 24 people; the 2nd experimental group of students in the major of “Agroengineering” included 25 people. The results of the survey are presented in Table 1, and the control testing in Table 2.
Table 1

<table>
<thead>
<tr>
<th>Questions</th>
<th>Responses, %</th>
</tr>
</thead>
<tbody>
<tr>
<td>Do you like programming in Assembler?</td>
<td>75 60</td>
</tr>
<tr>
<td>Does Assembler help you better understand a microcontroller operation?</td>
<td>67 68</td>
</tr>
<tr>
<td>Can you independently program the given algorithm in Assembly?</td>
<td></td>
</tr>
<tr>
<td>a) I can</td>
<td>33 24</td>
</tr>
<tr>
<td>b) I can, but only with the teacher’s or classmate’s help</td>
<td>66 76</td>
</tr>
</tbody>
</table>

Table 2

<table>
<thead>
<tr>
<th>Questions</th>
<th>Answers</th>
</tr>
</thead>
<tbody>
<tr>
<td>Name the microcontroller components participating in realization the algorithm of a programmable square wave generator …</td>
<td>o ADC, general purpose registers (GPRs ) block, analog comparator;</td>
</tr>
<tr>
<td></td>
<td>• GPRs block, program memory, parallel port;</td>
</tr>
<tr>
<td></td>
<td>o timer /counter, GPRs block, Analog comparator;</td>
</tr>
<tr>
<td></td>
<td>o non-volatile data memory, program memory, random access memory</td>
</tr>
<tr>
<td>How will the programmable generator frequency change if the number of DELAY subroutine cycles is increased?</td>
<td>o will increase;</td>
</tr>
<tr>
<td></td>
<td>• will decrease;</td>
</tr>
<tr>
<td></td>
<td>o remain the same;</td>
</tr>
<tr>
<td></td>
<td>o first increases, and then begins to decrease</td>
</tr>
<tr>
<td>What needs to be changed in the operation of the microcontroller without changing the program to reduce the frequency of the programmable generator?</td>
<td>o load another constant in a register of the GPRs block;</td>
</tr>
<tr>
<td></td>
<td>• increase the supply voltage;</td>
</tr>
<tr>
<td></td>
<td>o reduce the frequency of the clock generator;</td>
</tr>
<tr>
<td></td>
<td>o increase the frequency of the clock generator</td>
</tr>
<tr>
<td>Indicate the action sequence when developing the program in the AVR Studio environment …</td>
<td>o choosing a microcontroller, creating a new project, selecting a programming language, assigning a name to the project, specifying the project location;</td>
</tr>
<tr>
<td></td>
<td>• creating a new project, selecting a programming language, assigning a name to the project, specifying the project location, selecting a microcontroller;</td>
</tr>
<tr>
<td></td>
<td>o specifying the project location, creating a new project, selecting a programming language, assigning a name to the project, choosing a microcontroller;</td>
</tr>
<tr>
<td></td>
<td>o selecting a programming language, creating a new project, assigning a name to the project, specifying the project location, selecting a microcontroller</td>
</tr>
<tr>
<td>What basic parameters of a rectangular signal can be measured with an oscilloscope?</td>
<td>• frequency, pulse duration, pulse amplitude;</td>
</tr>
<tr>
<td></td>
<td>o period, pulse current, pulse amplitude;</td>
</tr>
<tr>
<td></td>
<td>o pause current, voltage, pulse duration;</td>
</tr>
<tr>
<td></td>
<td>• frequency, pause duration, voltage level of logical zero</td>
</tr>
</tbody>
</table>

The results of the experimental work can be subdivided into two groups:

1) Teachers’ results:
   - Elements of experimental work on formation of universal educational actions in the field of programming in Assembler are developed and tested;
• Training manual for training microcontroller programming in Assembler language is created.

2) Students’ results:
• Assembler allows a student to see the microcontroller internal devices in the form of graphic models and their interaction with each other, which increases the understanding level of the microcontroller structure and operation;
• The considered pulse generator example can be used for studying such aspects of AVR microcontroller architecture: program memory (FLASH), non-volatile memory (EEPROM), static random access memory (SRAM), and general purpose digital input / output ports.

Conclusions
1. The article presents an example of the organization of effective links between theoretical knowledge and practical skills formed in the bachelor’s training process at the engineering faculties.
2. The effectiveness of links is provided by using such cognitive principles as the inclusion of new material in the cognitive structure of the student’s personality (the connection of the new material with the knowledge and skills already available to a student), algorithmization, visualization and practical orientation of training acts.
3. Theoretical knowledge in the considered example is the knowledge of the microcontroller architecture, algorithms for implementing certain actions, methods of software development.
4. Practical skills are formed in the process of calculating the microcontroller program execution time and checking the obtained results in the AVR Studio debugging mode.
5. Experimental skills are formed by conducting real studies of the signal of the developed generator, the parameters of which were given at the beginning of the designing.
6. Knowledge and skills make the basis of the formed competence. Its further development presupposes the improvement of knowledge and skills in the process of creative realization in professional or social activity.

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